Schermatic Capture CAD Functional Comparison

	Quadcept Circuit Designer Community	Quadcept Circuit Designer Professional	A	В	С
Upfront cost (JPY)		0	224,000	900,000	350,000
Annual Maintenance Fee (JPY)	Free	0	10%~15%	10%~15%	10%~15%
Monthly Fee		\$39.00 (USD)	-	-	-
Туре	Non-Commercial	Commercial	Commercial	Commercial	Commercial
Target	Student, Hobbyist	Companies, Educational Institutions, Individual	-	-	-
Support	Online Manual Forum	Online Manual Forum (Priority Support) Accommodation Email Desk	Manual TEL/Mail (maintenance contract user only)	Manual TEL/Mail (maintenance contract user only)	Manual TEL/Mail (maintenance contract user only)
Version-Up and provide patch program	•	•	•	•	•
Free component library [Quadcept Share]	250,000 Free component Library	• 250,000 Free component Library			
Create component library service	×	•	-	-	-
os	WindowsVista Windows7/8/10	WindowsVista Windows7/8/10	WindowsXP WindowsVista Windows7/8/10	WindowsXP WindowsVista Windows7/8/10	WindowsXP WindowsVista Windows7/8
Digi-Key Connection	•	•	•	×	×
Customization of ribbon menu	•	•	×	×	×
Customization of toolbar and menu	•	•	×	×	×
Function of shortcut customization	•	•	×	×	×
Create original drawing frame	•	•	•	•	•
Stroke function	•	•	×	×	×
Memo function	•	•	×	•	×
Memo function (Tasks / Knowledge)	•	•	×	×	×
Reference name renumber	•	•	•	×	•
Selected reference name renumber (specific Ref name only)	•	•	×	×	•
Regular expression of search	•	•	×	•	×
PCB design direction (specification)	×	•	×	×	×
Back annotation of all Items (automatic)	×	•	•	•	•
Back annotation of all Items (manual)	•	•	×	×	•
PCB/Circuit drawing check function	•	•	×	×	×
Automatic calculation of component price	•	•	×	×	×
Display color setting	•	•	•	•	×
Display color setting of every wire	•	•	×	×	×
Display color setting of every net	×	•	•	▲(Segment)	•
Filter function	•	•	•	•	•
Undo/Redo	•	•	•	•	•

Mirror of component	•	•	•	•	•
Rotation of component	•	•	•	•	•
Select component gate	•	•	•	•	•
Alternate of component	•	•	•	•	•
Filtering search of component	•	•	•	•	•
Display switch function of user attribute	•	•	•	•	×
Move/Edit pins on the schematic	•	•	-	▲ (Available from Pinlist)	×
Edit component on the schematic	•	•	-	•	×
Change component size on the schematic	•	•	×	×	×
Change/Switch angle of wire	•	•	-	▲ (45°,90°)	×
Automatic wire connection when add component on the wire	•	•	-	×	×
Automatic wire function between 2 point	•	•	×	×	×
Bus parallel automatic function	•	•	×	×	×
Automatic wire occurred at overlap pins	•	•	×	×	×
Single point ground (include the connection with PCB)	•	•	×	×	×
Following wire of move pins	•	•	Δ	Δ	•
Automatic increment of label	•	•	•	•	•
Automatic address/Add pages function	•	•	•	•	•
Division polygon	•	•	×	×	×
Filled polygon	•	•	•	•	×
BOM export (possible to customize)	•	•	ОР	•	•
Dig-Key BOM function	•	•	•	×	×
Device Block	×	•	×	×	×
DRC	×	•	×	×	×
ERC	•	•	•	•	•
Design Direction	×	•	×	×	×
Data annotation of old/new circuit data (Confirmation of change)	×	•	×	×	×
DXF input/output	×	•	•	•	•
Changing Save Location of Design Data	×	•			
EDIF200 Support	•	•	•	•	×
All batch print function	×	•	×	•	×
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 $[\]label{eq:continuous} \mbox{\times'$/-: (not supported or unclear)} \mbox{\times No part of this document may be copied or used without permission $$\%$ Please be understanding that some function of each CAD may be resolved.}$